N THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant(s): T.J. Gabara et al.

Case:

91-2-36

Serial No.:

10/719,193

Filing Date:

November 21, 2003

Group:

2816

Examiner:

Jermele M. Hollington

Title:

Integrated Circuit with Controllable Test Access

to Internal Analog Signal Pads of an Area Array

## TRANSMITTAL LETTER

Alexandria, VA 22313-1450.

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450,

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Submitted herewith are the following documents relating to the above-identified patent application:

- (1) Pre-Appeal Brief Request for Review; and
- (2) Notice of Appeal.

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762 as required to correct the error. A duplicate copy of the Notice of Appeal is enclosed.

Respectfully submitted,

Date: April 10, 2006

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